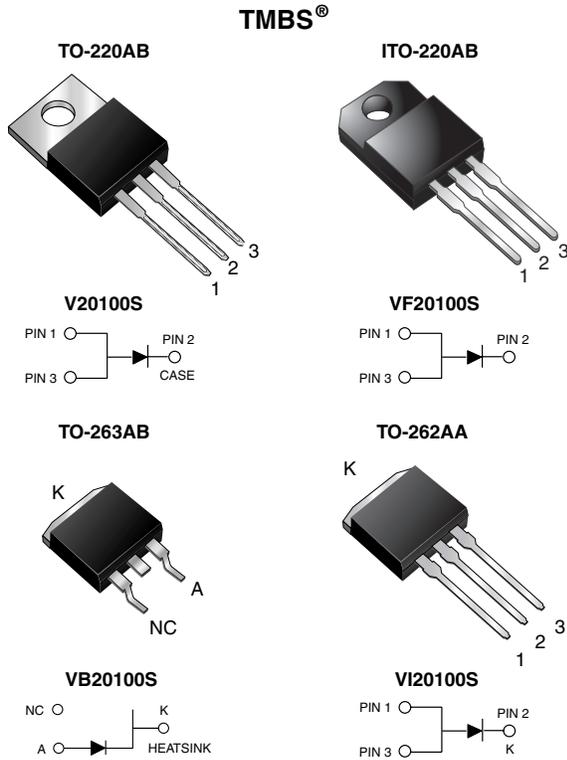


## High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low  $V_F = 0.446 \text{ V}$  at  $I_F = 5 \text{ A}$



### FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Low thermal resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for TO-263AB package)
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB, ITO-220AB, and TO-262AA package)
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC



**RoHS**  
COMPLIANT

### TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, dc-to-dc converters and reverse battery protection.

### MECHANICAL DATA

**Case:** TO-220AB, ITO-220AB, TO-263AB and TO-262AA

Molding compound meets UL 94 V-0 flammability rating

Base P/N-E3 - RoHS compliant, commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

**Polarity:** As marked

**Mounting Torque:** 10 in-lbs maximum

### PRIMARY CHARACTERISTICS

$I_{F(AV)}$	20 A
$V_{RRM}$	100 V
$I_{FSM}$	250 A
$V_F$ at $I_F = 20 \text{ A}$	0.69 V
$T_J \text{ max.}$	150 °C

### MAXIMUM RATINGS ( $T_A = 25 \text{ °C}$ unless otherwise noted)

PARAMETER	SYMBOL	V20100S	VF20100S	VB20100S	VI20100S	UNIT
Maximum repetitive peak reverse voltage	$V_{RRM}$		100			V
Maximum average forward rectified current (fig. 1)	$I_{F(AV)}$		20			A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	$I_{FSM}$		250			A
Non-repetitive avalanche energy at $T_J = 25 \text{ °C}$ , $L = 60 \text{ mH}$	$E_{AS}$		210			mJ
Peak repetitive reverse current at $t_p = 2 \text{ }\mu\text{s}$ , 1 kHz, $T_J = 38 \text{ °C} \pm 2 \text{ °C}$	$I_{RRM}$		1.0			A
Voltage rate of change (rated $V_R$ )	$dV/dt$		10 000			V/ $\mu\text{s}$
Isolation voltage (ITO-220AB only) From terminal to heatsink $t = 1 \text{ min}$	$V_{AC}$		1500			V
Operating junction and storage temperature range	$T_J, T_{STG}$		- 40 to + 150			°C

# V20100S, VF20100S, VB20100S & VI20100S



Vishay General Semiconductor

ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Breakdown voltage	I <sub>R</sub> = 10 mA	T <sub>A</sub> = 25 °C	V <sub>BR</sub>	105 (minimum)	-	V
Instantaneous forward voltage <sup>(1)</sup>	I <sub>F</sub> = 5 A I <sub>F</sub> = 10 A I <sub>F</sub> = 20 A	T <sub>A</sub> = 25 °C	V <sub>F</sub>	0.51 0.60 0.79	- - 0.90	V
	I <sub>F</sub> = 5 A I <sub>F</sub> = 10 A I <sub>F</sub> = 20 A	T <sub>A</sub> = 125 °C		0.45 0.53 0.69	- - 0.76	
Reverse current <sup>(2)</sup>	V <sub>R</sub> = 70 V	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	I <sub>R</sub>	17 7	- -	μA mA
	V <sub>R</sub> = 100 V	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C		70 14	500 30	μA mA

**Notes**

<sup>(1)</sup> Pulse test: 300 μs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	V20100S	VF20100S	VB20100S	VI20100S	UNIT
Typical thermal resistance	R <sub>θJC</sub>	2.0	4.0	2.0	2.0	°C/W

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-220AB	V20100S-E3/4W	1.88	4W	50/tube	Tube
ITO-220AB	VF20100S-E3/4W	1.75	4W	50/tube	Tube
TO-263AB	VB20100S-E3/4W	1.37	4W	50/tube	Tube
TO-263AB	VB20100S-E3/8W	1.37	8W	800/reel	Tape and reel
TO-262AA	VI20100S-E3/4W	1.45	4W	50/tube	Tube

**RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

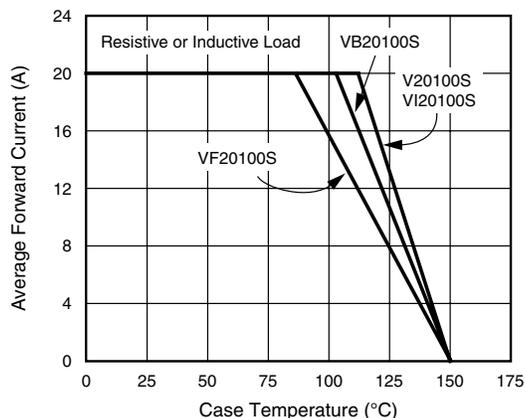


Figure 1. Maximum Forward Current Derating Curve

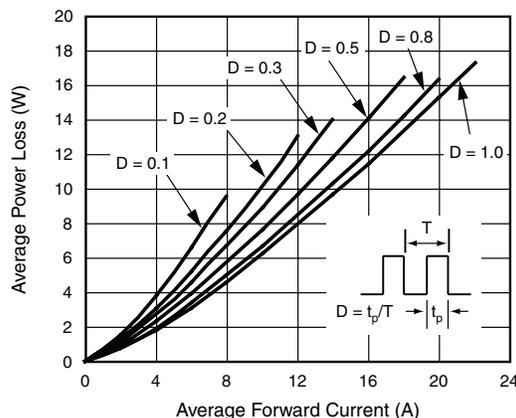


Figure 2. Forward Power Loss Characteristics

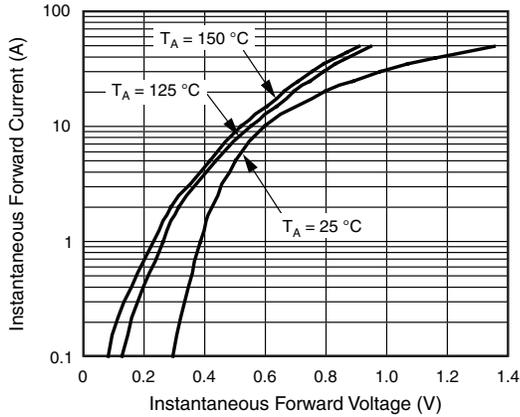


Figure 3. Typical Instantaneous Forward Characteristics

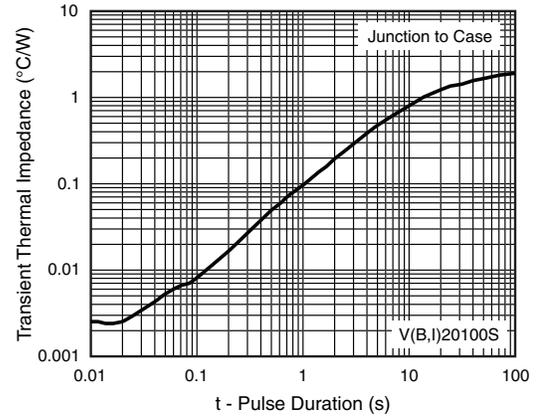


Figure 6. Typical Transient Thermal Impedance

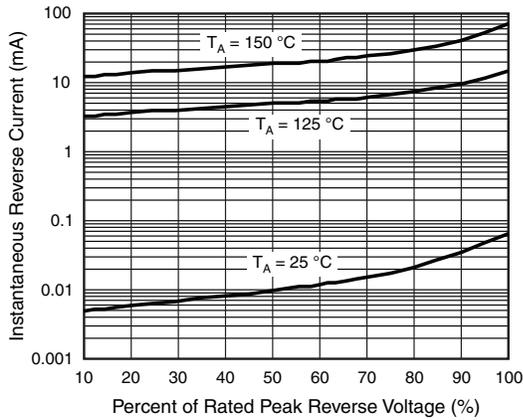


Figure 4. Typical Reverse Characteristics

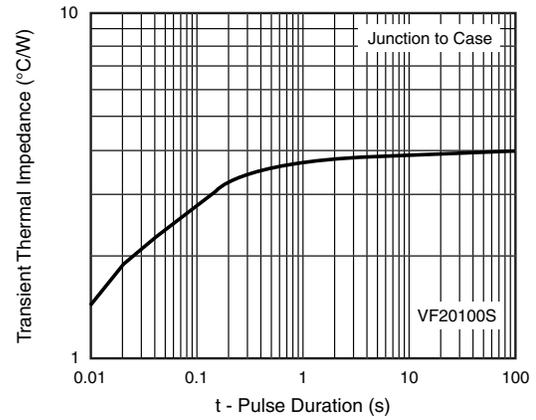


Figure 7. Typical Transient Thermal Impedance

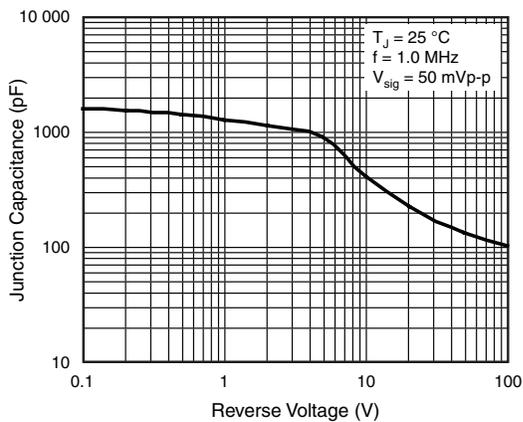


Figure 5. Typical Junction Capacitance





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